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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | | Application Number | 09/691,406 |
| | | | Filing Date | October 17, 2000 |
| | | | First Named Inventor | Christopher Hoover |
| | | | Group Art Unit | Not yet assigned |
| | | | Examiner Name | Not yet assigned |
| Sheet 1 of 1 | | | Attorney Docket Number | 21891.02500 |

| U.S. PATENT DOCUMENTS | | | | | | |
|-----------------------|-----------------------|-----------------------------|-----------------------------------|---|--|---|
| Examiner Initials* | Cite No. ¹ | U.S. Patent Document Number | Kind Code ² (if known) | Name of Patentee or Applicant Of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
| § | A1 | 5,963,447 | | Kohn et al. | 10/05/1999 | |
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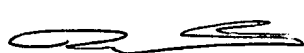
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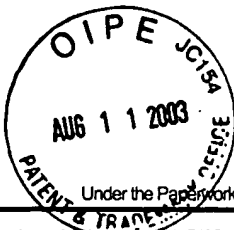
| FOREIGN PATENT DOCUMENTS | | | | | | | | |
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| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | | | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T ⁶ |
| | | Office ³ | Number ⁴ | Kind Code ⁵ (if known) | | | | |
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| OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS | | | |
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| § | C1 | Rogers, P. et al. (1997) "A Simulation Testbed for Comparing the Performance of Alternative Control Architectures," <i>Proceedings of the 1997 Winter Simulation Conference</i> , pp. 880-887 | |
| § | C2 | Oreizy, P. et al. (1999) "An Architecture-Based Approach to Self-Adaptive Software," <i>IEEE Intelligent Systems</i> , 14(3):54-62 | |
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| Examiner Signature |  | Date Considered | 03/31/2002 |
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PTO/SB/08b(05-03)

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Sheet 1 of 1

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|------------------------|--------------------|
| Application Number | 09/691,406 |
| Filing Date | October 17, 2000 |
| First Named Inventor | Christopher Hoover |
| Group Art Unit | 2123 |
| Examiner Name | Not assigned |
| Attorney Docket Number | 21891.02500 |


OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|---------------------|-----------------------|---|----------------|
| § / | AA | CASSAGNOL, BOB; et al., "Codesigning a Complex System on a Chip with Behavioral Models," November 1998, http://2www.isdmag.com/Editorial/1998/coverstory9811.html , 3/18/00. | |
| § / | AB | Cadence website, Cadence Press Release, "Addressing the Systems-in-Silicon Verification Challenge," http://www.cadence.com/press_box/na/pr/1995/inca.html , 3/18/00 | |
| § / | AC | Cadence website, "System-Level design," http://www.cadence.com/technology/hsw/vcc/html , 3/18/00 | |
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| Examiner Signature |  | Date Considered | 03/31/2004 |
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